Compound Semiconductor Materials Japan TC Chapter Meeting

In conjunction with SEMICON Japan 2026

Thursday, December 10, 2026

Room 905, TFT Building East Wing 9F, Tokyo, Japan/ Official Virtual TC Chapter Meeting (Hybrid)

1:00 PM － 3:00 PM JST

AGENDA

1 Welcome/Call to Order

1.1 Introduction

1.2 Required Elements

1.3 Agenda Review

2 Review of Previous Meeting Minutes

3 Liaison Report

3.1 Japan Regional Standards Committee (JRSC)

3.2 Global Coordinating Subcommittee (GCS)

3.3 Europe TC Chapter

3.4 North America TC Chapter

3.5 China TC Chapter

4 Staff Report

5 Ballot Review

None

6 Subcommittee & Task Force Reports

6.1 SiC Material and Wafer Task Force

7 Old Business

7.1 Project Period Review

7.2 5-year Review

8 New Business

9 Action Item Review

9.1 Open Action Items

Item #	*Assigned to*	*Details*
CSM20251218-01	Masayoshi Obara (TC chair)	To recommend the JRSC to appoint Makoto Kitabatake as a co-chair of the Compound Semiconductor Materials Japan TC Chapter.

9.2 New Action Items

Item #	*Assigned to*	*Details*

10 Next Meeting and Adjournment

10.1 The next meeting is scheduled for <date> at <event/location>.