

BIOGRAPHY

Global Semiconductor Market Insight Forum 2026



Name: Jiyoung Kim, Ph.D.

Title/Position: Professor, University-Industry Collaboration

Company: Department of Electrical and Computer Engineering, Seoul National University

Jiyoung Kim, Ph.D. has been Professor of University-Industry Collaboration at Seoul National University since 2025, with responsibility for bridging industrial semiconductor projects with academic research, leading collaborative innovation, and mentoring the next generation of engineers. He currently directs large-scale projects combining advanced DRAM, NAND, and AI semiconductor architectures with industry partners, building integrated ecosystems for Korea's semiconductor future.

Prior to joining Seoul National University, Kim was Vice President at Samsung Electronics, where he led product development for DRAM and HBM technologies. During his 27 years at Samsung, Kim managed DRAM device design from 170nm down to 14nm nodes, pioneered RCAT (Recess Channel Array Transistor) technology, and oversaw 3D memory integration. His leadership resulted in multiple industry-first products, including the world's first 14nm high-capacity ultra-fast DRAM, which earned the Korea Technology Presidential Award in 2022.

Kim received his Ph.D. in Electrical Engineering from the University of California, Los Angeles, USA, where his thesis on vertical multiple-stack transistors laid the foundation for ultra-high-density nonvolatile memory devices. He also holds an M.S. in Electronic and Electrical Engineering from POSTECH, Korea, and a B.S. in Physics from Hanyang University, Korea. He is the author of 8 journal papers, 12 international conference papers, and holds 230 registered patents.