

BIOGRAPHY

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Name: Anand Murthy PhD

Title/ Position: VP, Advanced Technology Integration, Office of the CTO

Company: Lam Research Corporation

Anand Murthy is the Vice President of Advanced Technology Integration within the Office of the CTO at Lam Research Corporation. Anand manages various process technology initiatives in Logic, Memory and 3DIC with several research consortia fostering strong integration and collaboration. Prior to joining Lam in 2024, Anand was an Intel Fellow with over 28 years of technical and leadership experience in advancing Moore's law of transistor scaling over 10 process nodes. Anand holds 340 U.S. (over 800 worldwide) patents granted for pioneering work in the fabrication of CMOS and HBT transistors based on Si, SiGe, Ge and III-V compound semiconductor materials. Anand graduated from University of Southern California with Ph.D. in Materials Science and Engineering in 1993.